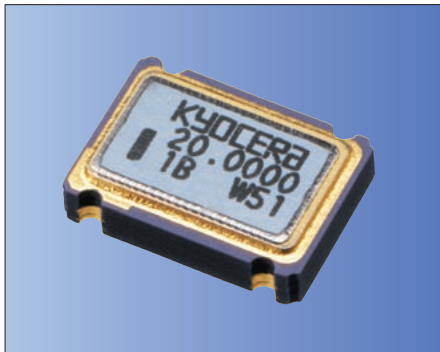


Clock Crystal Oscillators Surface Mount Type K50-3C Series



CMOS/ 3.3V/ 7.0×5.0mm

This product is NOT recommended for new designs.



Pb Free

RoHS Conforming

Features

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage $V_{DD}=3.3V$
- ± 25 ppm available

How to Order

K50-3C 0 - S E 25.0000
① ② ③ ④ ⑤

- ① Type(7×5 SMD, 3.3V)
- ② Frequency Stability Code(See Table1)
- ③ Duty Ratio(S: 45% to 55% STD)
- ④ Enable/Disable Function(STD)
- ⑤ Oscillation Frequency(Ex.: 25.0000MHz)

Packaging(Tape & Reel 1Kpcs/reel)

Table 1

Stability Code	(ppm)	T_{OPR} (°C)	Note
0	± 50	-10 to +70 (Standard)	Standard specifications
S	± 30		With only certain frequencies
U	± 25		With only certain frequencies
F	± 100	-40 to +85 (Extend)	With only certain frequencies
G	± 50		With only certain frequencies

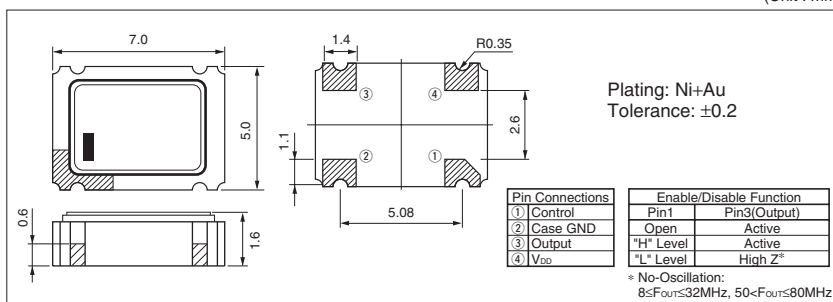
Specifications

Item	Symbol	Conditions	Min.	Max.	Units
Output Frequency Range	F_{OUT}		1.5	80	MHz
Frequency Stability	F_{SBY}	Overall conditions: initial tolerance, operating temperature range, rated power supply voltage change, load change, aging(1year @25°C), shock and vibration	-25 -30 -50	+25 +30 +50	ppm
Storage Temperature Range	T_{STG}		-55	+125	°C
Operating Temperature Range	T_{OPR}	Standard Extend(option)	-10 -40	+70 +85	
Max. Supply Voltage	—		-0.5	7.0	Volt
Supply Voltage	V_{DD}	Stability: ± 50 ppm, ± 30 ppm, ± 100 ppm(Ext Temp) Stability: ± 25 ppm, ± 50 ppm(Ext Temp)	2.97 3.14	3.63 3.46	
Current Consumption (Maximum Loaded)	I_{DD}	$1.5 \leq F_{OUT} \leq 20$ MHz $20 < F_{OUT} \leq 40$ MHz $40 < F_{OUT} \leq 60$ MHz $60 < F_{OUT} \leq 80$ MHz	— — — —	10 15 20 30	mA
Standby/Disable Current	I_{ST}/I_{DE}	$8 \leq F_{OUT} \leq 32$ MHz(Standby Function) $32 < F_{OUT} \leq 50$ MHz(Disable Function) $50 < F_{OUT} \leq 80$ MHz(Standby Function)	— — —	10 15 10	
Duty Ratio(Symmetry)	SYM	@ 50% V_{DD}	45	55	%
Rise/Fall Time (10% V_{DD} to 90% V_{DD} Maximum Loaded)	T_r/T_f	$8 \leq F_{OUT} \leq 26$ MHz $26 < F_{OUT} \leq 45$ MHz $45 < F_{OUT} \leq 80$ MHz	— — —	10 8 5	nS
Output Voltage-"L"	V_{OL}	$I_{OL}=8$ mA	—	10% V_{DD}	
Output Voltage-"H"	V_{OH}	$I_{OH}=8$ mA	90% V_{DD}	—	pF
Output Load	CL	CMOS	—	15	
Input Voltage Range	V_{IN}		0	V_{DD}	Volt
Input Voltage-"L"	V_{IL}		—	30% V_{DD}	
Input Voltage-"H"	V_{IH}		70% V_{DD}	—	mS
Output Disable Time	—		—	150	
Output Enable Time	—	$8 \leq F_{OUT} \leq 32$ MHz $32 < F_{OUT} \leq 50$ MHz $50 < F_{OUT} \leq 80$ MHz	— — —	5 150 5	mS
Start-up Time	ST	@ Minimum operating Voltage to be 0sec.	—	10	

Note: Please contact us for inquires about extended operating temperature range, available frequencies and other conditions.
All electrical characteristics are defined at the maximum load and operating temperature range.

Dimensions

(Unit : mm)



Recommended Land Pattern

(Unit : mm)

